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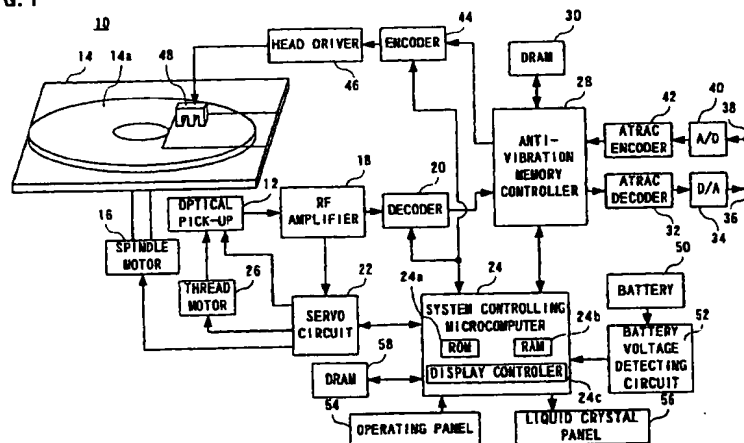
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### (54) Signal recording apparatus

(57) A signal recording apparatus includes a battery voltage detecting circuit. The battery voltage detecting circuit detects the voltage value of a battery. When a record button provided on an operation panel is pressed, a syscon detects, for 160 ms, voltage values given from the battery voltage detecting circuit. The detected voltage values are stored to a DRAM. An average is calculated of the voltage values of for 160 ms by using the DRAM. The syscon compares the averaged

voltage value with a threshold A that recording is possible for one disk. When the averaged voltage value is equal to or higher than the threshold A, the syscon directly starts recording. When the averaged voltage value is lower than the threshold A, an insufficient remaining capacity of the battery is displayed on a liquid crystal panel by a display controller.

FIG. 1



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## Description

### BACKGROUND OF THE INVENTION

#### Field of the invention

[0001] This invention relates generally to signal recording apparatuses and, more particularly, to a signal recording apparatus which is driven, for example, by a battery and applied for a recording medium, such as an MD, MO, DVD, CD-R, CD-RW or IC card, in which one or more of data and data management information are recorded.

#### Description of the prior art

[0002] There is one example of a conventional signal recording apparatus of this kind as disclosed in a publication of Japanese Patent Laid-open No. H5-54517 [G11B 19/00, G11B 20/10, G11B 27/10] laid open on March 5, 1993. In this prior art, if a battery voltage goes below a predetermined threshold during recording digital data onto an magnet-optical disk, TOC (Table Of Contents) information is recorded to the magnet-optical disk thus finishing the operation. In this manner, effective reproducing of record data is possible by recording TOC information in advance of running down of a battery.

[0003] However, if a higher voltage than an actual voltage be detected due to low accuracy, recording of digital data would continue even after the actual voltage becomes lower than the threshold. This however results in a problem that the operation is finished without recording TOC information. On the other hand, if the threshold is given higher in order to avoid such a problem, another problem occurs that the operation comes to an end before fully using the battery thus reducing recording time.

### SUMMARY OF THE INVENTION

[0004] Therefore, it is a primary object of the present invention to provide a signal recording apparatus which is capable of recording a desired signal completely.

[0005] A signal recording apparatus according to the present invention is a signal recording apparatus to be driven by a battery and record one or more of data and management information for managing the data to a recording medium, comprising: a detecting means for detecting a voltage value of the battery; a first comparing means for comparing detection result of the detecting means with a first threshold at which recording is possible to start; a second comparing means for comparing a detection result of the detecting means with a second threshold at which recording is possible to continue; a record starting means for starting to record the data depending upon a comparison result of the first

comparing means; and a record continuing means for continuing to record the data depending upon a comparison result of the second comparing means.

[0006] The signal recording apparatus is driven by a battery and records one or more of data and data management information to a recording medium. The detecting means detects a voltage value of the battery. The first comparing means compares a detected voltage value with a first threshold that recording is possible to start. Also, the second comparing means compared the detected voltage value with a second threshold that record is possible to continue. The record start means starts to record a signal to the recording medium depending on a comparison result of the first comparing means. During recording of data, the record continuing means continues the recording depending on a comparison result of the second comparing means. In this manner, it is possible to start and continue data recording depending on a battery voltage value.

[0007] According to the invention, when a voltage value is in that recording is possible to start, recording of data is started thus preventing the recording from finishing in the course thereof. It is therefore possible to completely record a desired signal.

[0008] For this reason, the first threshold is set at a value that recording can be completely made for the recording medium. In this case, the second management information recording means, when data recording is completed, records management information about all the data having been recorded before the completion of recording. Due to this, it is possible to positively reproduce all the recorded data.

[0009] In one aspect of the invention, a signal recording apparatus further comprises a caution means. When the detection result is lower than the first threshold, the caution means gives a caution of remaining capacity insufficiency of the battery. Accordingly, the user can charge or exchange the battery according to the caution.

[0010] In one embodiment of the invention, a signal recording apparatus further comprises a interruption means. When the voltage value is lower than the second threshold, the interruption means interrupts recording of data. Where data recording is interrupted in this manner, the first management information recording means records management information about all the data having been recorded before the interruption. Accordingly, even where the battery encounters insufficiency during recording data and finishes the recording in the course thereof, it is possible to reproduce the data having been so far recorded.

[0011] In this embodiment, the second threshold is set at a value that can record, after data record interruption, management information about all the data having been recorded before the interruption. Accordingly, even where there is necessity of suspending data recording, management information can be positively recorded. Therefore, the data recorded before interrup-

tion is possible to reproduce.

[0012] In another embodiment of the invention, a signal recording apparatus further comprises a determining means. The determining means, when recording of data is started in the signal recording apparatus, determines whether in waiting for data write or in writing data. Also, the second threshold includes a third threshold for a case of in waiting for writing data and a fourth threshold for a case of in writing data. Consequently, the interruption means finishes recording of the data when the voltage value during waiting for data writing is lower than the third threshold. Also, if the voltage value during data writing is lower than the fourth threshold, data recording is interrupted. In this manner, determination whether or not recording is possible to continue is made based on a different threshold depending on during data writing or in waiting for data writing. This makes determination higher in accuracy. It is therefore possible to effectively use the battery.

[0013] The above described objects and other objects, features, aspects and advantages of the present invention will become more apparent from the following detailed description of the present invention when taken in conjunction with the accompanying drawings.

## BRIEF DESCRIPTION OF THE DRAWINGS

### [0014]

Figure 1 is an illustrative view showing one embodiment of the present invention; and

Figure 2 is a flowchart showing one part of processing by system control microcomputer shown in the Figure 1 embodiment.

## DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0015] Referring to Figure 1, a signal recording apparatus 10 of this embodiment includes an optical pick-up 12. The optical pick-up 12 irradiates laser light to a backside of a disk 14a loaded on a mini-disk (MD) 14. Then, the optical pick-up 12 determines an N-pole or S-pole on a magnetic surface from a polarization plane of the reflection light from the disk 14a, and extracts a signal (audio signal) recorded on the disk 14a. Meanwhile, the disk 14a is rotatively driven by a spindle motor 16. The audio signal extracted through the optical pick-up 12 is supplied to an RF amplifier 18. The RF amplifier 18 amplifies the audio signal, and the amplified audio signal is delivered to a recorder 20 and to servo circuit 22. The recorder 20 subjects the audio signal given from the RF amplifier 18 to processing, such as decoding for error correction (parity error and interleave) and EFM (Eight to Fourteen Modulation) demodulation.

[0016] The servo circuit 22 controls the spindle

motor 16 to rotate based on the audio signal given from the RF amplifier 18 and according to an instruction by a system control microcomputer (syscon) 24. The servo circuit 22 also controls rotation of a thread motor 26 so that the optical pick-up 12 can be moved in a radial (thread) direction of the disk 14a. Furthermore, the servo circuit 22 controls such that a lens (not shown) provided on the optical pick-up 12 can correctly trace over a track (not shown) provided on the disk 14a. That is, the servo circuit 22 performs control of tracking servo and focus servo, according to an instruction given by the syscon 24.

[0017] The audio signal passed through the decoder 20 is stored to a DRAM 30, according to an instruction by an anti-vibration memory controller 28. The audio signal stored on the DRAM 30 is read out at a constant data rate according to an instruction by the anti-vibration memory controller 28 and then supplied to an ATRAC (Adaptive Transform Acoustic Coding) decoder 32, under control of the syscon 24. The ATRAC decoder 32 demodulates (decompresses) the compressed audio signal by an ATRAC scheme. The decompressed audio signal is supplied to a D/A converter 34. Thus, the audio signal as digital signal is converted into an analog signal to be outputted through an output terminal 36. In this manner, the signal recorded on the disk 14a is reproduced.

[0018] Incidentally, upon loading the MD 14, the TOC (Table Of Contents) information stored on the disk 14a is read out by the optical pick-up 12. This TOC information is stored into a RAM 24b according to an instruction by the anti-vibration memory controller 28. The syscon 24 reproduces an audio signal based on the TOC information. The TOC information includes such information as music piece count (file count), absolute time of recorded music pieces and absolute time of unrecorded region.

[0019] Meanwhile, an audio signal as analog data inputted onto an input terminal 38 is converted into digital data by an A/D converter 40 and supplied to an ATRAC encoder 42. The ATRAC encoder 42 encodes (compresses) the digital data of audio signal by an ATRAC scheme. The compressed audio signal is stored to the DRAM 30 according to the anti-vibration memory controller 28, under control of the syscon 24. The DRAM 30 is used as a buffer memory. The audio signal is controlled in time axis by the anti-vibration memory controller 28 and DRAM 30. That is, in order to record the audio signal to the disk 14a, the syscon 24 sends, as required, a read instruction to the anti-vibration memory controller 28. Accordingly, the audio signal stored on the DRAM 30 is read out in a burst fashion. The audio signal read out by the anti-vibration memory controller 28 is supplied to an encoder 44 where the encoder 44 performs encoding for error correction (parity addition and interleave processing), EFM coding, etc. on the signal.

[0020] The audio signal processed by the encoder

44 is given to head drive circuit 46. The head drive circuit 46 is connected to a magnetic head 48 so that it drives the magnetic head 48 to apply a modulation magnetic field to the disk 14a depending on the audio signal given from the encoder 44. Thereupon, the optical pick-up 12 is moved oppositely to the magnetic head 48 whereby thermomagnetic recording is made by a modulation magnetic field applied from the magnetic head 48 and the laser light given through the optical pick-up 12. During recording an audio signal, the syscon 24 forms TOC information about the audio signal by using the RAM 24b, in accordance with a situation of processing of the encoder 44. The signal recording apparatus 10, when completing the audio signal recording, makes sound recording finish after recording the TOC information.

**[0021]** Furthermore, the signal recording apparatus 10 includes a battery (secondary battery) 50, and is driven on power supplied from the battery 50. The battery 50 is connected with a battery voltage detector circuit 52. The battery voltage detector circuit 52 detects a voltage value of the battery 50. The voltage value detected by the battery voltage detector circuit 52 is supplied to the syscon 24. The syscon 24 controls to record an audio signal to the disk 14a, based on a voltage value given from the battery voltage detector circuit 52.

**[0022]** That is, when a record button (not shown) provided on an operation panel 54 is pressed, the syscon 24 detects a voltage value given from the battery voltage detector circuit 52. The syscon 24 compares this voltage value with a record-starting threshold A (6.5 V in this embodiment) record on the ROM 24a. Incidentally, the threshold A is a value empirically obtained, and is set at a value that can record 160-minute monaural audio signals together with TOC information for managing the audio signals to the disk 14a. For 160 ms, detection is made on voltage values given from the battery voltage detector circuit 52. The detected voltage values are delivered to the DRAM 58. The syscon 24 makes an average on the voltage values over the time period of 160 ms, and then compares the averaged voltage value with the threshold A. In this embodiment, the voltage values for the time period 160 ms are averaged for the purpose of absorbing errors of detection in voltage values.

**[0023]** When the averaged voltage value is equal to or higher than the threshold A, the syscon 24 starts recording audio signals. On the other hand, when the averaged voltage value is lower than the threshold A, a display controller 24c displays insufficiency of a battery 50 remaining capacity on a liquid crystal panel 56, according to an instruction by the syscon 24. That is, the syscon 24, when determining sound recording is impossible to record on one disk 14a, gives a caution of an insufficient remaining capacity of the battery 50 without performing sound-record operation. Accordingly, an operator is allowed to do sound recording again after

the battery 50 has been charged or exchanged. It is noted that an AC adapter (not shown) may be used in order to supply commercially-available power. Therefore, it is possible to prevent recording from finishing during the recording due to insufficient remaining capacity of the battery 50.

**[0024]** When sound recording is started, an audio signal is given to the input terminal 38 and recorded onto the disk 14a through the processing as described above. Meanwhile, the syscon 24, during also sound-recording an audio signal, detects voltage values given from the battery voltage detector circuit 52, and determines whether sound recording is possible to continue or not based on a detection result.

**[0025]** Explaining in greater detail, the syscon 24 determines whether the disk 14a is now under audio signal writing or not. In a state that the disk 14a is not being written by an audio signal (write waiting state), e.g. even during sound recording when the optical pick-up 12 or magnetic head 48 is in moving or the DRAM 30 is now under written by an audio signal, the voltage value is higher than that upon actually writing an audio signal (at a maximum output). Accordingly, the syscon 24 determines whether writing is possible to continue or not, based upon a threshold B (5.5 V) higher than a threshold C at a maximum output, hereinafter described. Incidentally, the threshold B is an empirically obtained value and stored within the ROM 24a. In also this case, an average value over a time period of 160 ms is used for a voltage value obtained from the battery voltage detector circuit 52. The syscon 24 determines that recording is possible to continue when the averaged voltage value is equal to or higher than the threshold B. However, when the averaged voltage value is lower than the threshold B, determination is made that recording is impossible to continue. In the case of a determination that recording is impossible to continue, the TOC information formed in the RAM 24b is recorded onto the disk 14a thus finishing the recording.

**[0026]** On the other hand, where sound recording is under continuing and the disk 14a is now being written by an audio signal, the syscon 24 compares the threshold C recorded in the ROM 24a (4.5 V in this embodiment) with the averaged voltage value. That is, when the disk 14a is now under written by an audio signal, the optical pick-up 12 is radiating laser light and accordingly the disk recording apparatus 10 assumes a maximum of load. Consequently, the battery 50 is maximum in output. Accordingly, the syscon 24 determines whether recording is possible to continue or not, based on comparison between the averaged voltage value and the threshold C lower than the threshold B. Incidentally, the threshold C is also an empirically obtained value and stored within the ROM 24a. When the averaged voltage value is equal to or greater than threshold C, determination is as possible to continue sound recording. However, where the averaged voltage value is lower than the threshold C, determination is made as impossibility to

continue sound recording. In the case of determining as impossibility to continue recording, the TOC information formed in the RAM 24b is recorded onto the disk 14a thus finishing sound recording.

[0027] In this manner, the threshold B and threshold C are set at respective values that, after the syscon 24 has determined impossibility to continue recording, the TOC information can be recorded to the disk 14a thereby finishing the recording.

[0028] The syscon 24 makes processing on the operations as described above, according to a flowchart shown in Figure 2. When the main power is turned on to the signal recording apparatus 10, the process is started. In step S1 it is determined whether the sound record button is pressed or not. If "NO" in step S1, it is determined that the sound record button is not pressed and the process returns directly to step S1. On the other hand, if "YES" in step S1, it is determined that the sound record button is pressed. In step S3 a voltage value of the battery 50 is detected and an average value of the voltage values for 160 ms is calculated. In the succeeding step S5, it is determined whether an averaged voltage value is equal to or higher than the threshold A or not. If "YES" in step S5, it is determined that recording is possible to start. In step S7 sound recording is started. On the other hand, if "NO" in step S5, it is determined that recording is impossible to start. In step S9 a caution is given of an insufficient remaining capacity of the battery 50, ending the process.

[0029] In step S11 it is determined whether recording of an audio signal has ended or not. If "YES" in step S11, it is determined that recording of an audio signal has finished, the process advancing to step S21. On the other hand, if "NO" in step S11, it is determined that audio signal recording has not yet finished, and in step S13 a battery 50 voltage value is detected to calculate an average value of the voltage values for 160 ms. In the succeeding step S15 it is determined whether the disk 14a is under written by an audio signal or not. If "NO" in step S15, determination is as in-waiting for data write and then in step S17 it is determined whether the averaged voltage value is equal to or higher than the threshold B or not. If "YES" in step S17, it is determined that recording is possible to continue and the process returned to step S11. On the other hand, if "NO" in step S17, it is determined that recording is impossible to continue and the process proceeds to step S21.

[0030] If "YES" in step S15, it is determined that the disk 14a is being written by an audio signal, and it is determined in step S19 whether the averaged voltage value is equal to higher than the threshold C or not. If "YES" in step S19, it is determined that sound recording is possible to continue, returning to step S11. On the other hand, if "NO" in step S19, it is determined that recording is impossible to continue, advancing to step S21. In step S21 the TOC information for managing the audio signal is recorded in the disk 14a, thus ending the process.

[0031] According to this embodiment, recording of an audio signal is made after determining whether recording is possible to start or not. It is therefore possible to prevent recording from finishing in the course thereof due to insufficiency of a battery remaining capacity. Meanwhile, because, during sound recording, the capability of continuing recording is determined using two thresholds depending upon a writing situation, thus enabling determination based upon an accurate battery remaining capacity. Furthermore, even where recording should be ended in the course thereof due to an insufficient battery remaining capacity, the recording is finished after recording the TOC information for managing the audio signals so far recorded. Thus, it is possible to positively reproduce the recorded audio signals.

[0032] Incidentally, in this embodiment the threshold A has been set at such a value that 160 minutes of monaural audio signals together with TOC information for managing the audio signals can be recorded to the disk 14a. Alternatively, the threshold A may be freely set depending on an unused region (recordable time) on the disk 14a.

[0033] Although this embodiment showed a signal recording apparatus using MD, it is needless to say that the invention is applicable also to a signal recording apparatus using MO, DVD, CD-R, CD-RW or IC cards.

[0034] Although the present invention has been described and illustrated in detail, it is clearly understood that the same is by way of illustration and example only and is not to be taken by way of limitation, the spirit and scope of the present invention being limited only by the terms of the appended claims.

## Claims

1. A signal recording apparatus to be driven by a battery and record one or more of data and management information for managing the data to a recording medium, comprising:

a detecting means for detecting a voltage value of said battery;

a first comparing means for comparing a detection result of said detecting means with a first threshold at which recording is possible to start;

a second comparing means for comparing a detection result of said detecting means with a second threshold at which recording is possible to continue;

a record starting means for starting to record the data depending upon a comparison result of said first comparing means; and

a record continuing means for continuing to record the data depending upon a comparison result of said second comparing means.

2. A signal recording apparatus according to claim 1, further comprising a caution means for giving a caution of remaining capacity insufficiency of said battery when the detection result is lower than the first threshold. 5
3. A signal recording apparatus according to claim 2, wherein the first threshold is a value that recording is possible completely to said recording medium. 10
4. A signal recording apparatus according to claim 1, further comprising a interruption means for suspending data recording when the detection result is lower than the second threshold; and a first management information recording means for recording, when the data is interrupted of recording, management information about all the data having been recorded before the interruption. 15
5. A signal recording apparatus according to claim 4, wherein said second threshold is a value that can record, after the data is interrupted of recording, management information about all the data having been recorded before the interruption. 20
6. A signal recording apparatus according to claim 4, further comprising a determining means to determine whether in waiting for writing data to said recording medium or in writing of data to said recording medium, wherein the second threshold includes a third threshold for a case of in waiting for data writing and a fourth threshold for a case of during data writing, and said interruption means finishing recording the data when the detection result of during data writing is lower than the third threshold and finishing recording the data when the detection result of in waiting for data writing is lower than the fourth threshold. 25 30 35
7. A signal recording apparatus according to claim 1, further comprising a second management information recording means for recording, when the data has been completely finished of recording, management information about all the data having been recorded before the complete finishing. 40 45

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FIG. 1

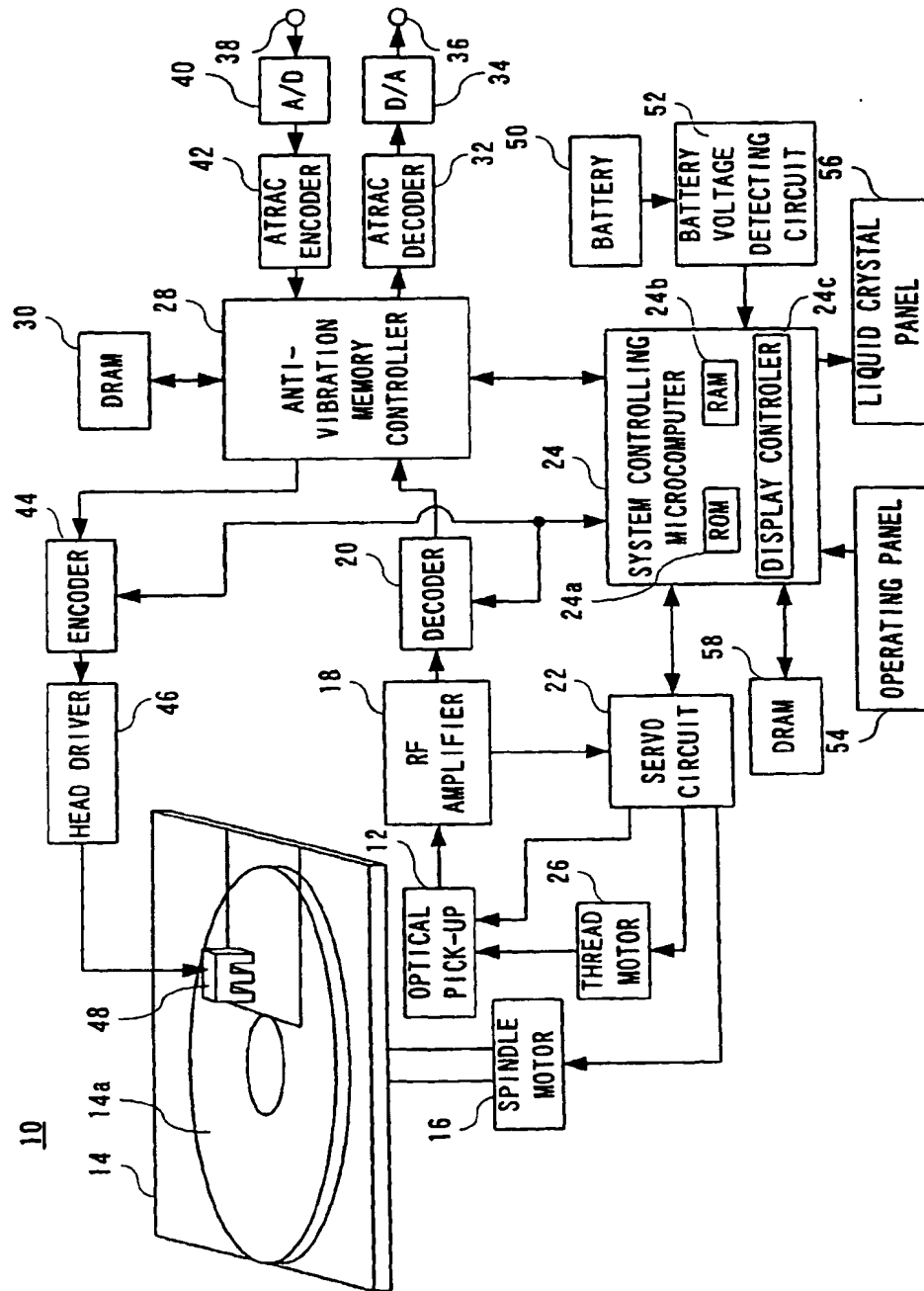
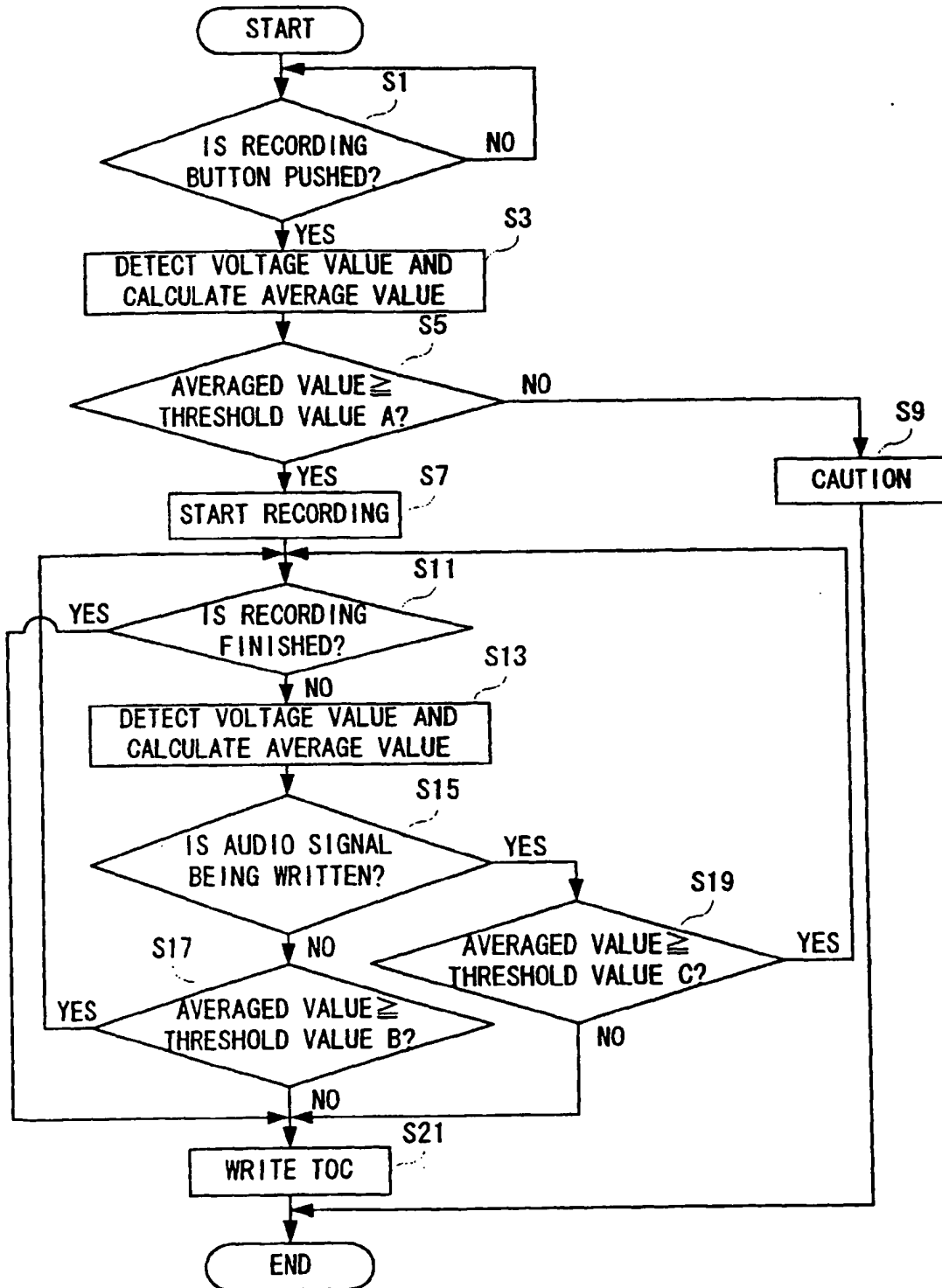


FIG. 2





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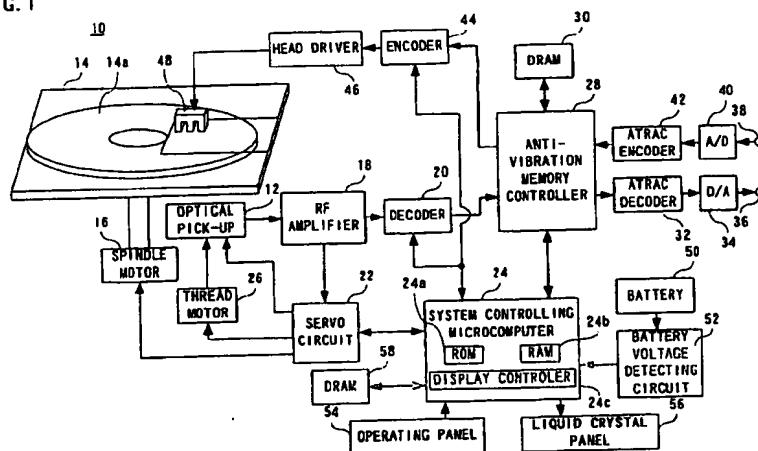
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**(54) Signal recording apparatus**

(57) A signal recording apparatus includes a battery voltage detecting circuit. The battery voltage detecting circuit detects the voltage value of a battery. When a record button provided on an operation panel is pressed, a syscon detects, for 160 ms, voltage values given from the battery voltage detecting circuit. The detected voltage values are stored to a DRAM. An average is calculated of the voltage values of for 160 ms by using the DRAM. The syscon compares the averaged

voltage value with a threshold A that recording is possible for one disk. When the averaged voltage value is equal to or higher than the threshold A, the syscon directly starts recording. When the averaged voltage value is lower than the threshold A, an insufficient remaining capacity of the battery is displayed on a liquid crystal panel by a display controller.

FIG. 1



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# EUROPEAN SEARCH REPORT

Application Number  
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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
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The present search report has been drawn up for all claims			
Place of search VIENNA		Date of completion of the search 20 July 2000	Examiner Dimitrow
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